

**WEST**

## Freeform Search

**Database:**

US Patents Full-Text Database  
US Pre-Grant Publication Full-Text Database  
JPO Abstracts Database  
EPO Abstracts Database  
Derwent World Patents Index  
IBM Technical Disclosure Bulletins

**Term:**

L11 and (ic or semiconductor or (integrated adj2  
circuit\$2)) and (writ\$3 and (releas\$3 with  
(sleep\$3)))

**Display:**  Documents in **Display Format:**  Starting with Number **Generate:** ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

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### Search History

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**DATE:** Sunday, August 10, 2003   [Printable Copy](#)   [Create Case](#)

**Set Name Query**

side by side

**Hit Count Set Name**

result set

*DB=USPT; PLUR=YES; OP=OR*

<u>L12</u>	L11 and (ic or semiconductor or (integrated adj2 circuit\$2)) and (writ\$3 and (releas\$3 with (sleep\$3)))	2	<u>L12</u>
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<u>L11</u>	((711/103 )!.CCLS. )	625	<u>L11</u>
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*DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR*

<u>L10</u>	(ic or semiconductor or (integrated adj2 circuit\$2)) and (writ\$3 and (releas\$3 with (sleep\$3)))	23	<u>L10</u>
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<u>L9</u>	(ic or semiconductor or (integrated adj2 circuit\$2)) and (writ\$3 same (releas\$3 with (sleep\$3)))	0	<u>L9</u>
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<u>L8</u>	(ic or semiconductor or (integrated adj2 circuit\$2)) and (releas\$3 with (sleep\$3))	66	<u>L8</u>
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*DB=USPT; PLUR=YES; OP=OR*

<u>L7</u>	L3 and (releas\$3 with (sleep\$3))	8	<u>L7</u>
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<u>L6</u>	L3 and (releas\$3 with (sleep\$3 or standby or hibernat\$3 or paus\$3))	40	<u>L6</u>
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<u>L5</u>	L3 and (reset\$4 with (ic or semiconductor or (integrated adj2 circuit\$2)) with (cpu or processor))	24	<u>L5</u>
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<u>L4</u>	L3 and (reset\$4 same (ic or semiconductor or (integrated adj2 circuit\$2)) same (cpu or processor))	60	<u>L4</u>
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<u>L3</u>	((365/\$)!.CCLS.)	33117	<u>L3</u>
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<u>L2</u>	L1 and (reset\$4 same (ic or semiconductor or (integrated adj2 circuit\$2)) same (cpu or processor))	1	<u>L2</u>
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<u>L1</u>	((365/52 )!.CCLS. )	326	<u>L1</u>
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END OF SEARCH HISTORY